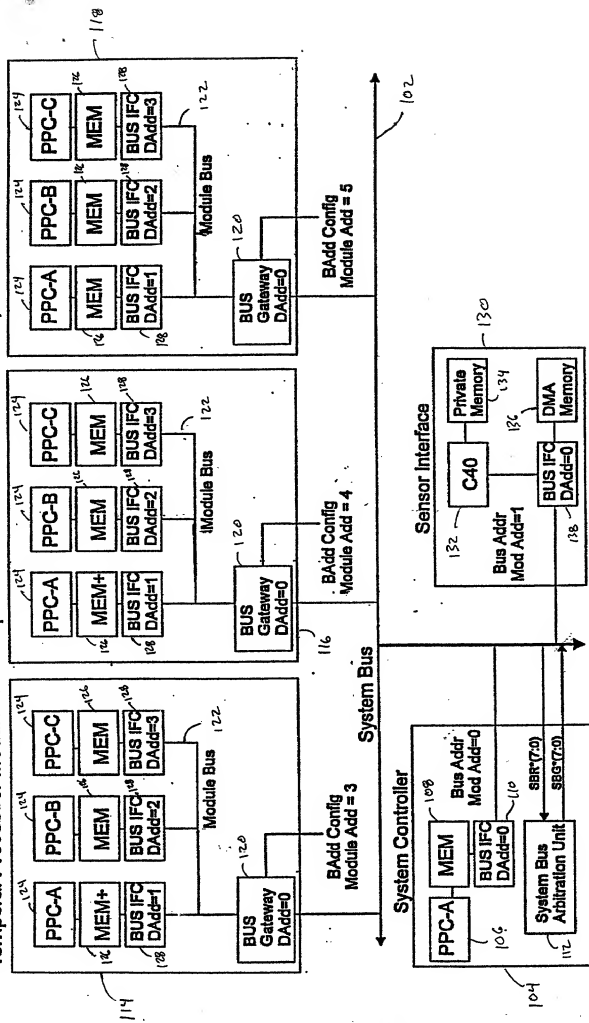
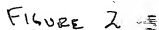


Temporal Processor Module A Temporal Processor Module B Temporal Processor Module C



APPL. FILING DATE: SEPTEMBER 20, 2001
 TITLE: SYSTEM BUS TRANSCIEVER INTERFACE
 INVENTOR(S): GREGORY S. ANDRE ET AL.
 APPLICATION SERIAL NO: UNASSIGNED



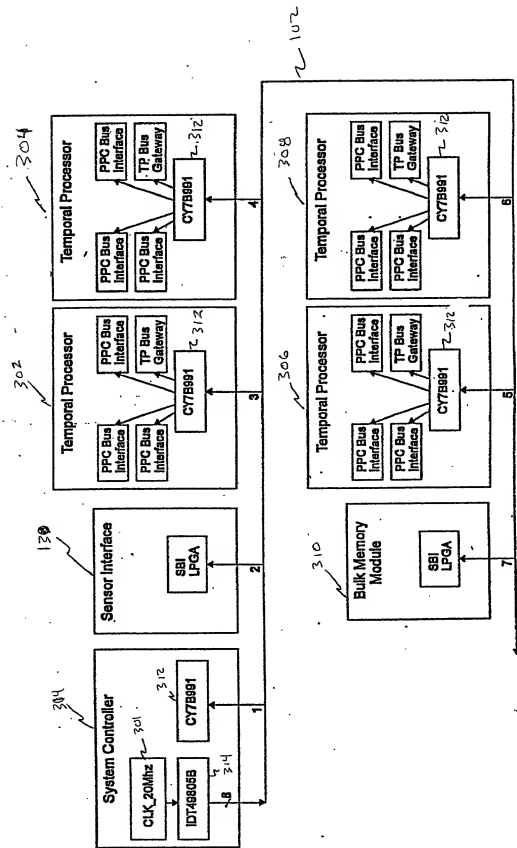


FIG. 3